

**LAKIREDDY BALI REDDY COLLEGE OF ENGINEERING (AUTONOMOUS)**L.B. Reddy Nagar :: Mylavaram-521 230 :: Krishna Dist. :: A.P
Approved by AICTE, New Delhi. Affiliated to JNTUK, Kakinada
EXAMINATION SECTION**A.Y. 2019-20****I MID EXAMINATION
B.Tech.(III-Semester) (R17)
For Lateral Entry Students (2019 admitted batch)****TIME TABLE****TIME : 9.00 AM – 10.30 AM**

DATE	ASE	CE	CSE	ECE	EEE	EIE	IT	ME
19-08-2019 (Monday)	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE08 - Probability and Statistics	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis
20-08-2019 (Tuesday)	17AE01 - Engineering Fluid Mechanics	17EE51 - Fundamentals of Electrical Engineering	17FE03 - Environmental Science	17CI01 - Computer Programming	17EE02 - Electric and Magnetic Fields	17FE03 - Environmental Science	17FE03 - Environmental Science	17FE03 - Environmental Science
21-08-2019 (Wednesday)	17AE02 - Engineering Thermodynamics	17CE04 - Strength of Materials - I	17CI03 - Discrete Mathematical Structures	17EC05 - Signals and Systems	17EE03 - Network Theory - I	17EE53 - Electrical Technology	17CI07 - OOPs through Java	17EC50 - Basic Electronics Engineering
22-08-2019 (Thursday)	17AE03 - Strength of Materials	17CE05 - Engineering Geology	17CI04 - Python Programming	17EC06 - Random Variables and Stochastic Processes	17EE04 - Digital Logic Circuit Design	17EI02 - Transducers	17IT01 - Operating System Principles	17ME03 - Thermodynamics
23-08-2019 (Friday)	17AE04 - Elements of Aerospace Engineering	17CE06 - Mechanics of Fluids	17CI05 - Data Structures	17EC07 - Pulse and Switching Circuits	17CI05 - Data Structures	17EC03 - Analog Electronic Circuits	17CI02 - Digital Logic Design	17ME04 - Mechanics of Solids
26-08-2019 (Monday)	17ME05 - Metallurgy and Material Science	17CE07 - Concrete Technology	17CI06 - Computer Architecture	17EC08 - Analog Integrated Circuits	17EE05 - Power Generation and Utilization	17EC04 - Digital Electronic Circuits	17CI09 - Data Base Management Systems	17ME05 - Metallurgy and Material Science
27-08-2019 (Tuesday)	17PD03 - Professional Ethics and Human Values	17PD03 - Professional Ethics and Human Values	---	17PD03 - Professional Ethics and Human Values	17PD03 - Professional Ethics and Human Values	---	---	---

SRE


CONTROLLER OF EXAMINATIONS

PRINCIPAL

Date: 07-08-2019

Copy to: 1. All Notice Boards
2. All H.O.Ds for N.A.,

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EXAMINATION SECTION

B.Tech.(III-Semester)(R17)
For Lateral Entry Students (2019 admitted batch)**ONLINE QUIZ EXAMINATION - I****A.Y. 2019-20****TIME TABLE**

DATE	ASE	CE	CSE	ECE	EEE	EIE	IT	ME
19-08-2019 (Monday)	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE08 - Probability and Statistics	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis	17FE07 - Numerical Methods and Fourier Analysis
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For Lateral Entry Students (2019 admitted batch)**

ONLINE QUIZ EXAMINATION - I

TIME TABLE

Following is the schedule for B.Tech.(III Semester)(R17) online quiz examination-I.

DATES	BRANCH	VENUE
	12.15 p.m - 12.45 p.m	
19-08-2019 to 27-08-2019	CSE	IPC lab (Block-I) Ground Floor
	IT	CDS lab (Block-I) First Floor
	EEE	Computational lab (Block-II) First Floor
	ME	CAD/CAM lab (Block-III) Ground Floor
	ECE	SP lab (Block-IV) Second Floor
	EIE	Systems lab (Block-V) Ground Floor
	ASE & CE	English lab (Block-V) First Floor

AMU
CONTROLLER OF EXAMINATIONS

A. V. S. R.
PRINCIPAL

SR